Compact 50 MHz High Power Solid State Amplifier using MRFE6VP61K25H

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This Article deals with RF amplifier design using high power RF solid state devices. If you have no experience working with these high power, please enlist the help of a qualified technician who has experience.
1. Introduction

Based in the article of F1JRD (AE7KX) Lionel, we have decided to build a 6 m module; all information’s below are still valid.

The MRFE6VP61K25H is a versatile device, well suited for a wide range of applications. It is capable of delivering 1.25KW under continuous wave test signal thanks to its high efficiency and low thermal resistance. It can also handle high VSWR condition.

This description is focused on 50 MHz radio band for both analog and digital waveforms. (SSB or WSJT/FSK/CW)

Main characteristics are:

- Frequency band: 50 - 52 MHz
- Output Power @ P3dB > 1250W
- Operating supply voltage: 50V
- Gain typ > 26 dB
- Eff typ > 78%
- PCB Size: 12cm x 7.3cm

The MRFE6VP61K25H is part of the 50 V product line, which is designed specifically for high voltage application by Freescale for the RF Power market, fabricated using Freescale’s proprietary Very High Voltage 6th generation with enhanced ruggedness (VHV6E) platform, this technology is fully qualified and ships in volume.

2. Advantages of 50V Drain Voltage

Using a 50V device is a great advantage compared to lower voltage because the output impedance of the device for the same output power is much greater, so the output match circuitry is simpler and less lossy. Current will also be lower compared to lower voltage.

In addition, 50V LDMOS provides increased power density, which enables in excess of 1.25kW in the industry standard NI1230 package platform.

50V LDMOS technology has also better IMD performances compared to lower voltage specially the 12V devices on bipolar technology.

The reference fixture was designed with the market standard drain supply, allowing the amplifier to utilize a standard 50V power supply (most are adjustable 43 to 54 V).

For all technology aspect refer to DUBUS magazine issues 4 / 2010 vol 39 "Compact 144 MHz High Power Solid State Amplifier using MRFE6VP61K25H" from AE7KX
3. Amplifier design and performance

The goal of this module is to be able to get 1 KW solid state, with good efficiency, small size and good handling to VSWR operation.

We based our design on the application note from Freescale MRFE6VP61K25HR6 FM broadcast (88 to 108 MHz).
For all thermal aspect please refer to (1).

The pcb is TC350 material witch have a very good RF and thermal performances.
The size of the amplifier is 120 x 72 mm.
The transistor has to dissipate more than 300 w in a small area, so a copper plate is necessary to transfer the heat to the aluminum heat sink.

Two techniques can be use to put the PCB on the copper heat spreader:
With screw like in the picture
With solder, this last technique needs some expertise and some solder paste (3).

Circuit description:
The transistor gain in low frequency is very high; we have to put some attenuation circuit to limit the gain.
The input circuit is realize with a 9:1 transformer like in the 2 m amplifier, because of the 6 m frequency a ferrite “E” and “I” have been added.
The frequency matching is tune by coil L1, L2, L3.
The 500 Ohms trimmer can be removing if an external bias circuit is use.
R5 , R6 , R7 , R8 are use to stabilize the input circuit; C4 and C5 are DC block.
1 K R9 and R10 are connected to ground to prevent a bias disconnection (if P1 is not use)
The output circuit is done also with a 9:1 transformer
A particular attention has to be done to manufacturing the 10 ohms coaxial cable Coax 1 and coax 2, see the manufacturing parts.

C6, C7, C8 are a high Q factor capacitor CLX series from TEMEX. C6 (75 pf) is a part of the matching, C7 and C8 are DC blocking.

This capacitor have a very low ESR and can handling more than 20 A RF current with 300 V for C7 and C8.

The output symmetrical balun is realized in 50 ohms Teflon coaxial RG142 and the same length of 16/10 wire, this balun improved the 2 nd harmonic.
4. RF measurements

The RF measurements have been done in CW and without the 3 dB input attenuator.

50 V Drain supply, 2000 mA Idq (For linear operation):
This test is done with the amplifier pallet only (without filter)

<table>
<thead>
<tr>
<th>Freq (MHz)</th>
<th>Pin (W)</th>
<th>Pout (W)</th>
<th>Gain (dB)</th>
<th>IRL (dB)</th>
<th>Eff (%)</th>
<th>Vd (V)</th>
<th>Id (A)</th>
</tr>
</thead>
<tbody>
<tr>
<td>50</td>
<td>0.1</td>
<td>100</td>
<td>30</td>
<td>-13</td>
<td>23.1</td>
<td>49.8</td>
<td>8.7</td>
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<tr>
<td>50</td>
<td>0.2</td>
<td>200</td>
<td>30</td>
<td>-13</td>
<td>33.5</td>
<td>49.8</td>
<td>12</td>
</tr>
<tr>
<td>50</td>
<td>0.3</td>
<td>300</td>
<td>30</td>
<td>-16</td>
<td>41</td>
<td>49.8</td>
<td>14.7</td>
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<tr>
<td>50</td>
<td>0.38</td>
<td>400</td>
<td>30.2</td>
<td>-17</td>
<td>47</td>
<td>49.8</td>
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<tr>
<td>50</td>
<td>0.51</td>
<td>500</td>
<td>29.9</td>
<td>-16</td>
<td>52.3</td>
<td>49.8</td>
<td>19.2</td>
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<tr>
<td>50</td>
<td>0.73</td>
<td>600</td>
<td>29.1</td>
<td>-16</td>
<td>56.3</td>
<td>49.8</td>
<td>21.4</td>
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<tr>
<td>50</td>
<td>0.88</td>
<td>700</td>
<td>29.0</td>
<td>-16</td>
<td>60.3</td>
<td>49.8</td>
<td>23.3</td>
</tr>
</tbody>
</table>

| 50 P1 dB   | 1       | 800      | 29.0      | -16      | 64.3    | 49.8   | 25    |
| 50         | 1.4     | 900      | 28.1      | -16      | 66.9    | 49.8   | 27    |
| 50         | 2       | 1000     | 27        | -16      | 70      | 49.8   | 28.7  |

| 50 P6dB    | 4.6     | 1100     | 23.8      | -17      | 72.4    | 49.8   | 30.5  |

The harmonic test show H2 @ -40 dBc and H3 @ -10 dBc, this mean about 100 W of 150 MHz.
A filter is absolutely necessary to get the harmonics below 60 dBc.
We have test with a filter describe by F1TE some year ago (3),

[Image]

The result with the (amplifier + 91 cm RG142 + filter) is in the new table below (without the input attenuator):

<table>
<thead>
<tr>
<th>Freq (MHz)</th>
<th>Pin (W)</th>
<th>Pout (W)</th>
<th>Gain (dB)</th>
<th>IRL (dB)</th>
<th>Eff (%)</th>
<th>Vd (V)</th>
<th>Id (A)</th>
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<td>0.2</td>
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<td>30</td>
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<tr>
<td>50</td>
<td>0.4</td>
<td>400</td>
<td>30</td>
<td>-16</td>
<td>48.1</td>
<td>49.8</td>
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<tr>
<td>50</td>
<td>0.5</td>
<td>500</td>
<td>30</td>
<td>-17</td>
<td>53.4</td>
<td>49.8</td>
<td>18.8</td>
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<tr>
<td>50</td>
<td>0.6</td>
<td>600</td>
<td>30</td>
<td>-16</td>
<td>57.4</td>
<td>49.8</td>
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<td>0.70</td>
<td>700</td>
<td>30</td>
<td>-16</td>
<td>61.7</td>
<td>49.8</td>
<td>22.8</td>
</tr>
<tr>
<td>50</td>
<td>0.88</td>
<td>800</td>
<td>29.6</td>
<td>-16</td>
<td>65.3</td>
<td>49.8</td>
<td>24.6</td>
</tr>
</tbody>
</table>

| 50 P1 dB   | 1       | 900      | 29.5      | -16      | 67.9    | 49.8   | 26.6  |
| 50         | 1.4     | 1000     | 28.5      | -16      | 71.7    | 49.8   | 28    |
| 50         | 1.8     | 1100     | 27.9      | -16      | 74.1    | 49.8   | 29.8  |

| 50 P4dB    | 2.8     | 1200     | 26.3      | -17      | 76      | 49.8   | 31.7  |
This last result shows an improvement in efficiency and output power. What that mean?? The 3rd harmonic is fully reflected to the transistor and the amplification class is not any more AB, we are closer to class F or FG this is the reason of the 80 % of efficiency.
But to get this result the length of the coaxial cable between the amplifier and the filter need to be adjusted. And if the length is not correct, the result can be very bad and can destroy the transistor.
As an example if we had 30 cm more to the existing cable the efficiency is not more than 60 % and the power is limiting to 900 W.

**This mean for each type of filter design, the length of the cable have to be adjust for the better efficiency.**
For the filter used I have to had 91 cm of RG142BU

A new design have to be test to improved H3, it have to be design with a 4:1 output balun and some H2 and H3 trap.

5. IMD measurement

The IMD mesure was done in the same condition than the 2 m amplifier

Pout : 800 W PEP
Idq : 2 A
IMD3 : - 28 dBc / PEP
IMD5 : - 47 dBc / PEP

7. Ruggedness

MRFE6VP61K25H is a very rugged part capable of handling 65:1 VSWR in pulse mode.
It was designed for high mismatch applications, such as laser and plasma exciters, that exhibit repetitive high VSWR values at startup and then come back to a more friendly impedance.
In CW, at these VSWR level and rated power, the limiting factor is the max DC power dissipation.

VSWR protection that shutdown the gate voltage within 10mS on a single shot will protect the transistor effectively.
The amplifier presented here, was tested all phase angle 10mS pulse 5% duty cycle without any failure or degradation in RF performance.

Construction:

Layout and bill of material
### Fixture layout and placement

#### Tab1. Bill Of materials

<table>
<thead>
<tr>
<th>Part</th>
<th>Description</th>
<th>Part Number</th>
<th>Supplier</th>
</tr>
</thead>
<tbody>
<tr>
<td>B1, B2</td>
<td>Bead Long Fair-Rite (95 ohm@100MHz)</td>
<td>2743021447</td>
<td>Fair-Rite</td>
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<tr>
<td>L1</td>
<td>3 Turn air inductor on 6 mm diameter, Tinned copper wire of 10/10mm diameter</td>
<td></td>
<td></td>
</tr>
<tr>
<td>L2, L3</td>
<td>1.5 Turn air inductor on 3 mm diameter, Tinned copper wire of 6/10mm diameter</td>
<td></td>
<td></td>
</tr>
<tr>
<td>L4, L5</td>
<td>7 Turn air inductor on 10 mm diameter, Enamelled copper wire of 16/10mm diameter</td>
<td></td>
<td></td>
</tr>
<tr>
<td>C1</td>
<td>1 nF ATC 100B Ceramic Capacitor</td>
<td>ATC100B102KT500XT</td>
<td>ATC</td>
</tr>
<tr>
<td>C2, C3, C4, C5</td>
<td>100 nF Ceramic Capacitor 30 V</td>
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<td>SMD</td>
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<td>C6</td>
<td>75 pF CLX Ceramic Capacitor</td>
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<td>TEMEX</td>
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<td>C7, C8</td>
<td>2.7 nF CLX Ceramic Capacitor</td>
<td>CLX</td>
<td>TEMEX</td>
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<tr>
<td>C9, C10, C13, C14</td>
<td>1 nF Ceramic Capacitor 100 V</td>
<td></td>
<td>LCC</td>
</tr>
<tr>
<td>C11, C15</td>
<td>100 nF Ceramic Capacitor 100 V</td>
<td></td>
<td>LCC</td>
</tr>
<tr>
<td>C12, C16</td>
<td>100 uF 160V</td>
<td></td>
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<td>R1, R3</td>
<td>330 ohm 3 W Power Resistor</td>
<td>PR03</td>
<td>Vishay</td>
</tr>
<tr>
<td>R2</td>
<td>18 ohm 3 W Power Resistor</td>
<td>PR03</td>
<td>Vishay</td>
</tr>
<tr>
<td>R4</td>
<td>5.6 ohm 1 W Chip Resistor</td>
<td>2512</td>
<td>Vishay</td>
</tr>
<tr>
<td>R5, R6, R7, R8</td>
<td>33 ohm 3 W Power Resistor</td>
<td>PR03</td>
<td>Vishay</td>
</tr>
<tr>
<td>R9, R10</td>
<td>1 Kohm 1 W Chip Resistor</td>
<td>2512</td>
<td>Vishay</td>
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<tr>
<td>P1</td>
<td>500 Ohms SMD pot</td>
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<td>Bourns</td>
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<td>T1</td>
<td>Balun TUI-9 Comm Concepts</td>
<td>TUI-9</td>
<td>Com Concepts or RF Power Systems</td>
</tr>
<tr>
<td>E, I</td>
<td>Ferrite for TUI-9 Comm Concepts</td>
<td>E, I</td>
<td>Com Concepts or RF Power Systems</td>
</tr>
<tr>
<td>Coax1, Coax2</td>
<td>Flex Cable (10.2 Ohm) 25cm</td>
<td>TC-12</td>
<td>Com Concepts or RF Power Systems</td>
</tr>
<tr>
<td>W1</td>
<td>35 cm 2 1 40 mm diameter Enamelled copper wire of 16/10mm diameter</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Coax3</td>
<td>Coax cable, 50ohm, 35 cm</td>
<td>RF142 BU</td>
<td></td>
</tr>
<tr>
<td>PCB</td>
<td>Arlon TC350, 2 OZ 30 mils</td>
<td>PA6-1K</td>
<td>RFham.com</td>
</tr>
</tbody>
</table>
1) put the PCB on the copper plate

2) mount SMD components and PR03 resistors

3) Prepare the 9:1 input balun

A alternative is to use a binocular 43 mix ferrite a description is available (3)
4) put the balun in place

5) stick the two “I” with cyanolite glue and just one point of glue in the “E” and PCB

6) prepare the 10 ohms coaxial cable

Length: 25 cm

7) next step the output balun
8) **Before soldering the transistor it is much better to make some small test.**

a) Set the pot to zero.

b) With a DVM measure in the resistance between the gate pad and ground

R = 5.6 ohms

c) If you have a “network analyzer” or any type like mini VNA you can measure the output matching of the amplifier (this method is also use for tube amplifier).

Put a 5.6 ohms and a 180 pF capacitor between the two drain pad

The output match is about 20 dB (see the curve below)

9) **Transistor placement :**

Two technique can be use:

a) the transistor can be solder, in fact this is the better technique to have a very low thermal resistance
disadvantage if the transistor is destroy for any reason it will be very difficult to change

b) use silicon grease (the white one for a lower thermal resistance)

before any thing, the transistor have to be prepare

the flange of the transistor is not very “flat”, the best way to do it, it is to use abrasive paper type “1200”

the abrasive paper have to be put in a very flat surface like glass

with the transistor make 8 with your hand until the flange is very flat

Put the transistor in place.

10) **Bias adjustment**

Connect the output to a 50 ohms load

Use two power supply:

One for the bias 0 to 12 V

Set to 5 V (the pot is set to ground)

Set the drain power supply to 50 V (the best is to use a 2 A max PSU)

Adjust the Idq up to 2 A
11) Small signal test:
If you have a network analyzer or similar, test the amplifier in small signal otherwise in very low output power with the appropriate load.

Set the input VSWR by tuning L1, L2, L3
The input return loss is better than 15 dB
The small signal gain is around 28 dB

If you didn’t get this value don’t put the amplifier in full power, you may be have a wrong output wiring

![Small signal gain](image1)
![Output VSWR](image2)

12) High power test:
The best way to keep the transistor alive, the best way is to attenuate the input power of the amplifier.
Most of the 50 MHz transceiver have a output power of 100 W, the power can be set to 10 W but “if you forget” you will put 100 W at the input of amplifier and destroy the transistor.
In addition many amateur transceiver with power control turn down have bad ALC and transmit a big spike at the beginning of each transmission.

Several technique can be use, like F1TE have describe in is web site (3) or to use a 100 W 10 dB pad and a small 3 dB resistor pad install in the pcb.

Set the power the output power in the range of 300 W and check the drain current:
it have to be in the range of 14 A,
if it is not the case, stop and verified the output balun.
If it is ok, you can increase the power up to 1KW.

Don’t forget to use protections, several description are available now (3) (4)
Kit of main components is available (5)

Conclusion
The board was duplicated several times and each board show similar results, no need to re-tune the board to get the expected performances.
But be careful high power amplifier can always be dangerous.
Many thanks to F1JRD Lionel, F4CWN Florent, Thomas Jann, F5VHX Graham
The next description will be a 70 cm amplifier.

References
MRFE6VP61K25 datasheet
(1) A compact 144 MHz High Power Solid State Amplifier using the MRFE6VP6125KH
(2) AN1034D Freescale (ex TRW application note)
(3) WWW.f1te.org ; WWW.f5cys.eklablog.com/
(4) WWW.vk4dd.com/
(5) WWW.rfham.com